

Amendments to the Specification:

Please replace the paragraph beginning on page 9, line 7, with the following amended paragraph:

The system controller 110 is coupled to several of the memory modules 54a,b...n through the high-speed link 58. The processor ~~194~~ 104 accesses some of the memory modules 54 in the computer system 100 in a data format optimized for use as main memory. One of the memory modules 54 is directly accessed by the graphics controller 112, and this memory module is configured in the high bandwidth mode, as previously explained.